

**Features:**

- Isolated mounting base 3000V~
- Solder joint technology with increased power cycling capability
- Space and weight saving

Typical Applications:

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

V _{DRM} , V _{R_{RM}}	Type & Outline
600V	DSKT160/06E-229H3/229H3B
800V	DSKT160/08E-229H3/229H3B
1000V	DSKT160/10E-229H3/229H3B
1200V	DSKT160/12E-229H3/229H3B
1400V	DSKT160/14E-229H3/229H3B
1600V	DSKT160/16E-229H3/229H3B
1800V	DSKT160/18E-229H3/229H3B

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _J (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Single side cooled, T _c =85°C	125			160	A
I _{T(RMS)}	RMS on-state current		125			251	A
I _{DRM} I _{R_{RM}}	Repetitive peak current	at V _{DRM} at V _{R_{RM}}	125			20	mA
I _{TSM}	Surge on-state current	10ms half sine wave	125			4.68	kA
I ² t	I ² t for fusing coordination	V _R =60%V _{R_{RM}}				110	A ² s*10 ³
V _{TO}	Threshold voltage		125			0.8	V
r _T	On-state slope resistance					1.77	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =480A	25			1.6	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =67%V _{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t _r ≤0.5μs Repetitive	125			200	A/μs
I _{GT}	Gate trigger current	V _A =12V, I _A =1A	25	30		150	mA
V _{GT}	Gate trigger voltage			0.6		2.5	V
I _H	Holding current			10		150	mA
I _L	Latching current					1000	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125			0.2	V
R _{th(j-c)}	Thermal resistance Junction to case	Single side cooled per chip				0.172	°C /W
R _{th(c-h)}	Thermal resistance case to heatsink	Single side cooled per chip				0.10	°C /W
V _{iso}	Isolation voltage	50Hz, R.M.S, t=1min, I _{iso} :1mA(MAX)		2500			V
F _m	Terminal connection torque(M6)			4.5	5	5.5	N·m
	Mounting torque(M6)			4.5	5	5.5	N·m
T _{vj}	Junction temperature			-40		125	°C
T _{stg}	Stored temperature			-40		125	°C
W _t	Weight				165		g
Outline	229H3、229H3B						

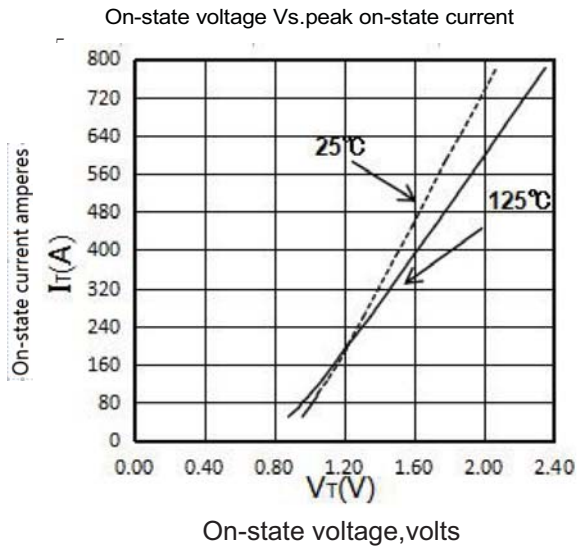


Fig1

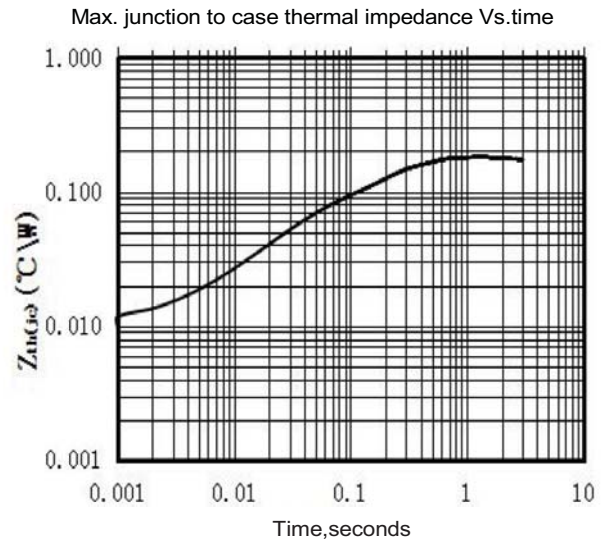


Fig2

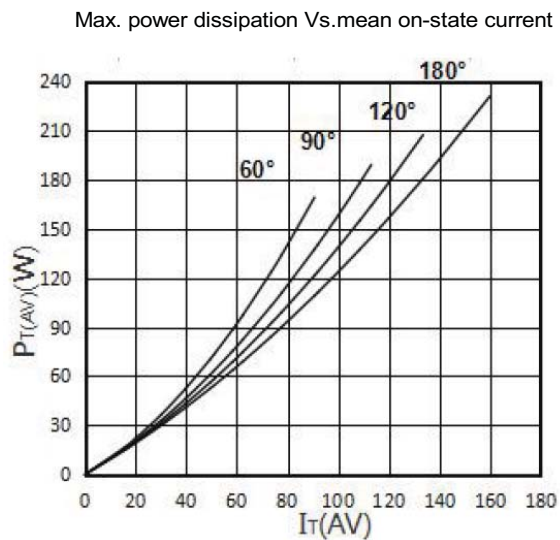


Fig3

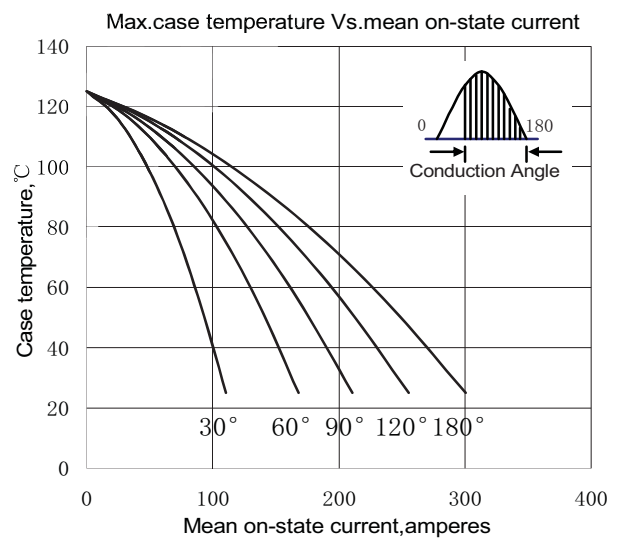


Fig4

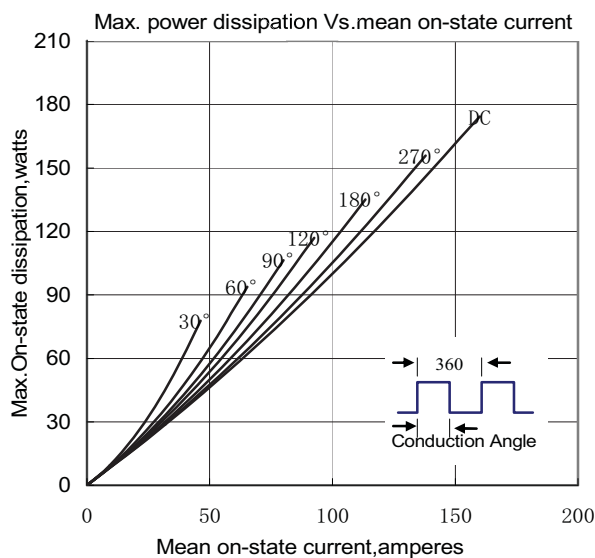


Fig5

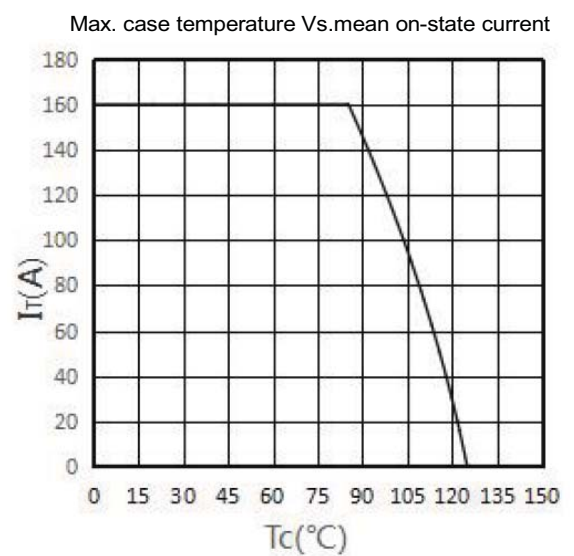


Fig6

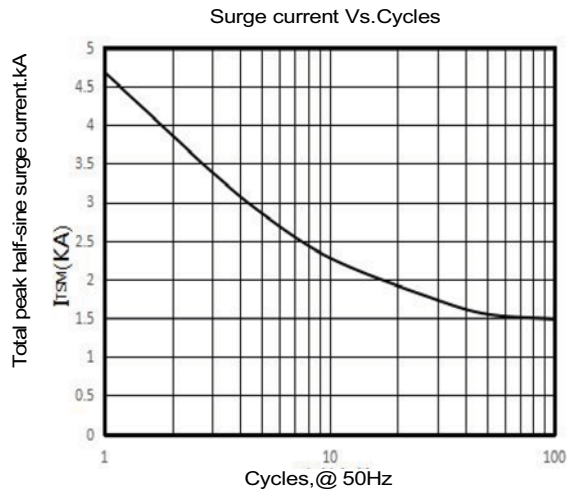


Fig7

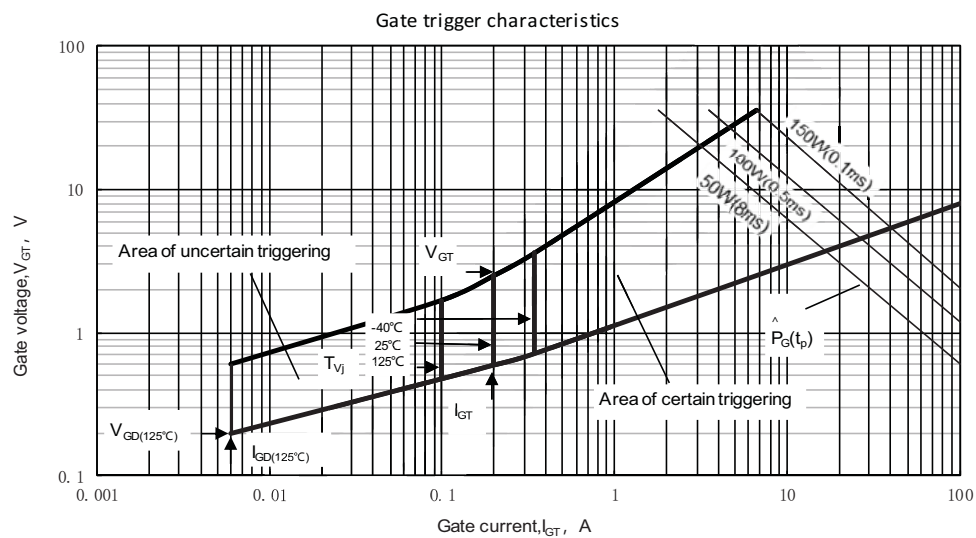
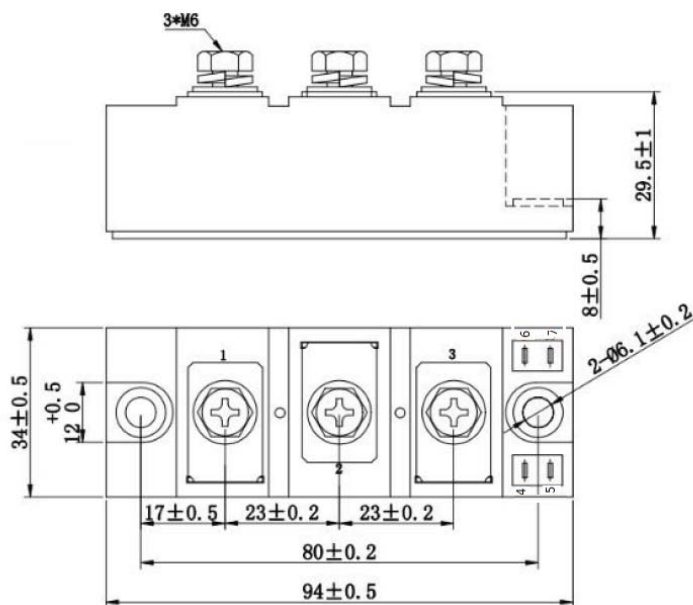


Fig8

Outline:

Unmarked dimensional tolerance: $\pm 0.5\text{mm}$ 